PTO/SB/96 (07-09)

Approved for use through 07/31/2012. OMB 0651-0031

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STATEMENT UNDER 37 CFR 3.73(b)			
Applicant/Patent Owner: Cadence Design	ın Systems, Inc.		
Application No./Patent No.: 7103816		Filed/Issue Date:	September 5, 2006
Titled: Method And System for Reducing Test Data Volume in the Testing of Logic Products			
Cadence Design Systems, Inc. (Name of Assignee) , a Corporation (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)			
states that it is:			
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2. an assignee of less than the entire right, title, and interest in			
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3. an assignee of an undivided interest in the entirety of (a complete assignment from one of the joint inventors was made)			
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Frank Distler, Leonard Andrew Ferko, Brion I 1. From: Koenemann, Donald V	Keller, Bernd	To : Internationa	l Business Machines Corp.
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The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.			
/Jeffrey A. Miller/			May 19, 2011
Signature			Date
Jeffrey A. Miller Printed or Typed Name	9		Attorney for Assignee Title
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